

A Practical Introduction To Hardware Software Codesign

G Psacharopoulos

A Practical Introduction to Hardware/Software Codesign Patrick R. Schaumont, 2010-09-09 This is a practical book for computer engineers who want to understand or implement hardware/software systems. It focuses on problems that require one to combine hardware design with software design – such problems can be solved with hardware/software codesign. When used properly, hardware/software co- sign works better than hardware design or software design alone: it can improve the overall performance of digital systems, and it can shorten their design time. Hardware/software codesign can help a designer to make trade-offs between the flexibility and the performance of a digital system. To achieve this, a designer needs to combine two radically different ways of design: the sequential way of decomposition in time, using software, with the parallel way of decomposition in space, using hardware. Intended Audience This book assumes that you have a basic understanding of hardware that you are familiar with standard digital hardware components such as registers, logic gates, and components such as multiplexers and arithmetic operators. The book also assumes that you know how to write a program in C. These topics are usually covered in an introductory course on computer engineering or in a combination of courses on digital design and software engineering.

A Practical Introduction to Hardware/Software Codesign Patrick R. Schaumont, 2012-11-27 This textbook serves as an introduction to the subject of embedded systems design, with emphasis on integration of custom hardware components with software. The key problem addressed in the book is the following: how can an embedded systems designer strike a balance between flexibility and efficiency? The book describes how combining hardware design with software design leads to a solution to this important computer engineering problem. The book covers four topics in hardware/software codesign: fundamentals, the design space of custom architectures, the hardware/software interface and application examples. The book comes with an associated design environment that helps the reader to perform experiments in hardware/software codesign. Each chapter also includes exercises and further reading suggestions. Improvements in this second edition include labs and examples using modern FPGA environments from Xilinx and Altera, which will make the material in this book applicable to a greater number of courses where these tools are already in use. More examples and exercises have been added throughout the book. “If I were teaching a course on this subject, I would use this as a resource and text. If I were a student who wanted to learn codesign, I would look for a course that at least used a similar approach. If I were an engineer or engineering

manager who wanted to learn more about codesign from a very practical perspective, I would read this book first before any other. When I first started learning about codesign as a practitioner, a book like this would have been the perfect introduction.” --Grant Martin, Tensilica--

A Practical Introduction to Hardware/Software Codesign Patrick Schaumont, 2011-03-02 This is a practical book for computer engineers who want to understand or implement hardware/software systems. It focuses on problems that require one to combine hardware design with software design – such problems can be solved with hardware/software codesign. When used properly, hardware/software co- sign works better than hardware design or software design alone: it can improve the overall performance of digital systems, and it can shorten their design time. Hardware/software codesign can help a designer to make trade-offs between the flexibility and the performance of a digital system. To achieve this, a designer needs to combine two radically different ways of design: the sequential way of decomposition in time, using software, with the parallel way of decomposition in space, using hardware. **Intended Audience** This book assumes that you have a basic understanding of hardware that you are familiar with standard digital hardware components such as registers, logic gates, and components such as multiplexers and arithmetic operators. The book also assumes that you know how to write a program in C. These topics are usually covered in an introductory course on computer engineering or in a combination of courses on digital design and software engineering.

A Practical Introduction to Hardware/Software Codesign Springer, 2012-11-26

Embedded SoPC Design with Nios II Processor and Verilog Examples Pong P. Chu, 2012-04-30 Explores the unique hardware programmability of FPGA-based embedded systems, using a learn-by-doing approach to introduce the concepts and techniques for embedded SoPC design with Verilog. An SoPC (system on a programmable chip) integrates a processor, memory modules, I/O peripherals, and custom hardware accelerators into a single FPGA (field-programmable gate array) device. In addition to the customized software, customized hardware can be developed and incorporated into the embedded system as well allowing us to configure the soft-core processor, create tailored I/O interfaces, and develop specialized hardware accelerators for computation-intensive tasks. Utilizing an Altera FPGA prototyping board and its Nios II soft-core processor, *Embedded SoPC Design with Nios II Processor and Verilog Examples* takes a learn by doing approach to illustrate the hardware and software design and development process by including realistic projects that can be implemented and tested on the board. Emphasizing hardware design and integration throughout, the book is divided into four major parts: Part I covers HDL and synthesis of custom hardware Part II introduces the Nios II processor and provides an overview of embedded software development Part III demonstrates the design and development of hardware and software of several complex I/O peripherals, including a PS2 keyboard and mouse, a graphic video controller, an audio codec, and an SD (secure digital) card Part IV provides several case studies of the integration of hardware accelerators, including a custom GCD

(greatest common divisor) circuit, a Mandelbrot set fractal circuit, and an audio synthesizer based on DDFS (direct digital frequency synthesis) methodology While designing and developing an embedded SoPC can be rewarding, the learning can be a long and winding journey. This book shows the trail ahead and guides readers through the initial steps to exploit the full potential of this emerging methodology.

Proceedings of the ... International Symposium on Hardware/Software Codesign ,2001

Real Time Programming 1994 Wolfgang A. Halang,1994 In recent years the interest in all aspects of real-time computing has increased significantly. This is not only due to accelerated research efforts undertaken in this area, but also due to an expanding worldwide market for various types of real time computing systems. This publication brings together state-of-the-art research from around the world, which makes a significant contribution to the current analysis and future development of this key subject. Sections covering distributed systems, scheduling, verification and validation concepts and architecture, operating systems and software development, ensure that all major aspects are fully represented.

Proceedings of the Third International Workshop on Hardware/Software Codesign ,1994 The proceedings of the September 1994 workshop comprise 28 technical papers that represent several important trends in co-design research: use of design case studies to drive research; algorithms for hardware- software partitioning; algorithms for system verification and validation; and a continuing interest in design representations. No index. Annotation copyright by Book News, Inc., Portland, OR.

Proceedings ,1996

IEEE/ACM/IFIP International Conference on Hardware/Software Codesign & System Synthesis ,2003

**First IEEE/ACM/IFIP International Conference on Hardware/Software Codesign & System Synthesis ,2003
Embedded Software ,2004**

Proceedings of the ... Midwest Symposium on Circuits and Systems ,1998

Proceedings Euromicro. Conference,1998

Proceedings, ... International Symposium on VLSI Design ,1997

11th Euromicro Workshop on Real-Time Systems ,1999

Tenth International Conference on VLSI Design ,1997

The Codesign of Embedded Systems: A Unified Hardware/Software Representation Sanjaya Kumar,James H. Aylor,Barry W. Johnson,Wm.A. Wulf,2012-12-06 Current practice dictates the separation of the hardware and software development paths early in the design cycle. These paths remain independent with very little interaction occurring between them until system integration. In particular, hardware is often specified without fully appreciating the computational requirements of the software. Also, software development does not influence hardware development and does not track

changes made during the hardware design phase. Thus, the ability to explore hardware/software tradeoffs is restricted, such as the movement of functionality from the software domain to the hardware domain (and vice-versa) or the modification of the hardware/software interface. As a result, problems that are encountered during system integration may require modification of the software and/or hardware, resulting in potentially significant cost increases and schedule overruns. To address the problems described above, a cooperative design approach, one that utilizes a unified view of hardware and software, is described. This approach is called hardware/software codesign. The Codesign of Embedded Systems develops several fundamental hardware/software codesign concepts and a methodology that supports them. A unified representation, referred to as a decomposition graph, is presented which can be used to describe hardware or software using either functional abstractions or data abstractions. Using a unified representation based on functional abstractions, an abstract hardware/software model has been implemented in a common simulation environment called ADEPT (Advanced Design Environment Prototyping Tool). This model permits early hardware/software evaluation and tradeoff exploration. Techniques have been developed which support the identification of software bottlenecks and the evaluation of design alternatives with respect to multiple metrics. The application of the model is demonstrated on several examples. A unified representation based on data abstractions is also explored. This work leads to investigations regarding the application of object-oriented techniques to hardware design. The Codesign of Embedded Systems: A Unified Hardware/Software Representation describes a novel approach to a topic of immense importance to CAD researchers and designers alike.

Periodica Polytechnica, 2002

Rapid System Prototyping (RSP 2000) IEEE Computer Society, IEEE Computer Society Staff, 2000 The 11th International Workshop on Rapid System Prototyping was held in 2000. These proceedings cover: communication and distributed systems; reconfigurable architectures; partitioning, scheduling and performance analysis; design methodologies; interface technologies; and more.

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