

Space Vector Pulse Width Modulation

Mohamed Ghibril Md Yasan

Pulse Width Modulation for Power Converters D. Grahame Holmes, Thomas A. Lipo, 2003-10-03 * The first single volume resource for researchers in the field who previously had to depend on separate papers and conference records to attain a working knowledge of the subject. * Brings together the field's diverse approaches into an integrated and comprehensive theory of PWM

Design & Implementation of Space Vector Pulse Narendrasinh C. Rana, 2019-06-13

Pulse Width Modulation Satish Kumar Peddapelli, 2016-12-19 This book offers a general approach to pulse width modulation techniques and multilevel inverter topologies. The multilevel inverters can be approximately compared to a sinusoidal waveform because of their increased number of direct current voltage levels, which provides an opportunity to eliminate harmonic contents and therefore allows the utilization of smaller and more reliable components. On the other side, multilevel inverters require more components than traditional inverters and that increases the overall cost of the system. The various algorithms for multilevel neutral point clamped inverter fed induction motor are proposed and implemented, and the results are analyzed. The performance of these algorithms is evaluated in terms of inverter output voltage, current waveforms and total harmonic distortion. Various basic pulse width modulation techniques, features and implementation of space vector pulse width modulation for a two-level inverter, and various multilevel inverter topologies are discussed in detail. This book is extremely useful for undergraduate students, postgraduate students, industry people, scientists of research laboratories and especially for the research scholars who are working in the area of multilevel inverters. Dr. Satish Kumar Peddapelli is Assistant Professor at the Osmania University in Hyderabad, India. His areas of interest are Power Electronics, Drives, Power Converters, Multi Level Inverters and Special Machines.

Space Vector Based Pulse Width Modulation (PWM) Technique Özgür Cemal Özerdem, Doğu Akdeniz Üniversitesi, 1994

Modified Space Vector PWM Sequences for High Power Drives Abhishekam Koppula, Sri Gowri Kolli, 2015-02-12

The Space Vector Pulse Width Modulation technique generally we have algorithms based on the angle information. This requires more computational efforts. In this book focused on the simplified algorithm for the SVPWM Techniques without considering angle information. This contains four types of switching sequences, over these we can which one gives better performance. The comparison of the sequences defined based on the type samples (odd and even samples) per sector. The

odd number of samples will gives better performance.

Study on Comparison Between Voltage Control Space Vector Pulse Width Modulation and Current Control Space Vector Pulse Width Modulation on Vector Control for Three Phase Induction Motor Mohamed Ghibril Md Yasan,2012

Advanced Pulse-Width-Modulation: With Freedom to Optimize Power Electronics Converters Dong Jiang,Zewei Shen,Qiao Li,Jianan Chen,Zicheng Liu,2021-01-20 This book is a technical publication for students, scholars and engineers in electrical engineering, focusing on the pulse-width-modulation (PWM) technologies in power electronics area. Based on an introduction of basic PWM principles this book analyzes three major challenges for PWM on system performance: power losses, voltage/current ripple and electromagnetic interference (EMI) noise, and the lack of utilization of control freedoms in conventional PWM technologies. Then, the model of PWM's impact on system performance is introduced, with the current ripple prediction method for voltage source converter as example. With the prediction model, two major advanced PWM methods are introduced: variable switching frequency PWM and phase-shift PWM, which can reduce the power losses and EMI for the system based on the prediction model. Furthermore, the advanced PWM can be applied in advanced topologies including multilevel converters and paralleled converters. With more control variables in the advanced topologies, performance of PWM can be further improved. Also, for the special problem for common-mode noise, this book introduces modified PWM method for reduction. Especially, the paralleled inverters with advanced PWM can achieve good performance for the common-mode noise reduction. Finally, the implementation of PWM technologies in hardware is introduced in the last part.

Space Vector Pulse Width Modulation in Three-level Inverter Muhammad Sufyan,2013

Space Vector Modulation of Multi-Level Converters and Application Omeje Crescent Onyebuchi,Agu Marcel,2015-08-31 This book presents the analysis of 3, 4 and 5- level diode clamped converter that is based on the space-vector pulse width modulation technique which is considered as the most efficient and digitally applied modulation scheme. This technique was compared with another controlled modulation method which is based on the multi-carriers/sinusoidal pulse-width modulation with an injected third order harmonic signal. It also focuses on the step by step development of MATLAB/SIMULINK modeling of the space vector pulse-width modulation and the multi-carriers/sinusoidal pulse width modulation with an injected third harmonic based on the 3, 4 and 5-level inverter waveforms at 0.4, 0.6 and 0.8 modulation indices and their corresponding spectral results. The MATLAB simulation results obtained from the direct application of 3-level Voltage Source Inverter VSI on 10HP, 400 Volts, 50Hz induction machine drive are also presented in subchapter 3.4. In line with the above analysis is a small scale laboratory designed-experimental work that was constructed to verify the simulation results obtained from the SIMPLORER simulations for the un-modulated three-level diode clamped converter.

Real Time Implementation of Space Vector Based Pulse Width Modulation (PWM) Technique Attila Alışkan, Doğu Akdeniz Üniversitesi, 1995

The Implementation of Space Vector Modulation Using Microcontroller in Generating Pulse Width Modulation Signal Mohd. Shahril Md. Isa, 2006

Investigation of Space Vector Pulse Width Modulation Strategies for Three-level T-type Converters with Solar Power Applications Xiong Li, 2015 In a solar power conversion system, power electronics converters are extensively used to interface the solar power of different power levels for various applications. The dc/ac inverter is the key to provide safe, efficient, and reliable power conversion. Historically, efficiency, reliability, and unit cost have been the three primary metrics that designers of string solar inverters have sought to optimize. The insulated-gate bipolar transistor (IGBT) based two-level dc/ac topology is the standard industry solution to meet the requirements of the applications described. In recent years, however, size and weight have proven to significantly affect overall system cost, and have subsequently been added to the list of essential design metrics. Switching frequency boosting is an effective approach to size down a converter system. However, the power conversion efficiency drops significantly at high switching frequency where the switching loss dominates in conventional two-level converters. It has been proved that multilevel converters have a flat dependence of the efficiency on the switching frequency. In addition, a multilevel converter offers the benefits including high power conversion efficiency, low harmonic distortion, low electromagnetic interference (EMI) noise, low leakage current, high reliability etc. Three-level converters, especially three-level T-type converter (TLT2C) and three-level neutral point clamped converter (TLNPC), are becoming popular for solar power applications because of the merits mentioned above. A TLT2C combines the positive aspects of the two-level converter with the advantages of the TLNPC converter. The superior features of the TLT2C make it especially suitable for low voltage, high efficiency, and high switching frequency solar power applications. With the help of silicon carbide (SiC) devices, the TLT2C topology can achieve a super high power conversion efficiency (> 99%) with a high switching frequency. The performance enhancement approaches of a solar inverter are not limited to new topologies and semiconductor technologies, but can be extended through advanced pulse width modulation strategies. The goal of this research is to develop advanced space vector pulse width modulation (SVPWM) strategies to enhance the system performance, which includes aspects of power conversion efficiency, power density, power quality, system cost, and reliability of a TLT2C based solar inverter.

Space Vector Methods for AC Drives to Achieve High Efficiency and Superior Waveform Quality, 2004 Space vector approach to pulse width modulation offers several advantages over the traditional triangle comparison methods, which are not fully exploited by conventional space vector PWM (SVPWM). New sequences, which divide the active vector duration, offer additional degrees of freedom that are not possible in triangle comparison approach. The objectives of this research are

to identify% all such new sequences and develop new PWM techniques that reduce distortion and switching loss. Four new sequences involving active state division are introduced. The concept of stator flux ripple is used to design THD-optimized PWM techniques. By applying different sequences within a sector based on above techniques, THD can be reduced by 47% compared to conventional SVPWM. The effects of sequences on inverter switching loss are analyzed to design a new hybrid PWM that reduces switching loss by 30%. This involves using sequences that clamp the phase with the highest current, and double switch the phase with lowest current. Finally, a combined hybrid PWM technique that simultaneously reduces THD by over 30% and switching loss by 20%, under nominal conditions is developed. The new techniques have been implemented in a 2kW induction motor drive controlled by a DSP - TMS320F243. THD reduction of 38% in the THD-optimized PWM and switching loss reduction of 30% in the loss-optimized PWM are demonstrated. Future work in this area may focus on extending the concept to multi-level inverters.

Enhanced Space Vector Pulse Width Modulation Technique and "step-up" Criteria Based Modulation Technique for Multi-level Inverters Shahar-Shlomo Levy,2017 The aim of this research is to develop enhanced inverter modulation algorithm to achieve close to optimal performance results with reasonable complexity. -- page 2.

Controlling the Vienna Rectifier Using a Simplified Space Vector Pulse Width Modulation Technique Ali Sunbul,2019 In this thesis, a simplified Space-Vector Pulse-Width-Modulation (SVPWM) technique for the Vienna rectifier is introduced to reduce the computational burden, the switching losses and the Total-Harmonic-Distortion (THD). Furthermore, the robustness of this modulation technique is tested under various faults through a 70 kW MATLAB/Simulink model and the results are validated through 1.2 kW prototype. The results reveal that the simplified SVPWM provides a low THD, unity Power-Factor (PF) and effective capacitor voltage balancing even after extreme faults. This study introduces a multilevel Power-Factor-Correction (PFC) converter in a 2-stage configuration. The first stage is the Vienna rectifier which has a high boosting ratio. To overcome this issue, a high efficiency 4-switch converter is cascaded with the Vienna rectifier. This converter employs storage-less passive components and provides a Zero-Current-Switching (ZCS) for all of its switches. A description of the converter is first introduced followed by the simulation results.

Instruments, Measurement, Electronics and Information Engineering J.Z. Ma,2013-08-08 Selected, peer reviewed papers from the 2013 International Conference on Precision Mechanical Instruments and Measurement Technology (ICPMIMT 2013), May 25-26, 2013, Shenyang, Liaoning, China

A Novel Control Scheme for a Pole-changing Induction Motor Drive John William Kelly,2007

Mapping of Pulse Width Modulation Algorithms Into Architecture Tuhin Ray,1989

Progress in Renewable and Sustainable Energy Yong Guang Li,Yong Li,Wei Guo Pan,2012-12-13 Selected, peer reviewed papers from the 2nd International Conference on Energy, Environment and Sustainable Development (EESD 2012),

October 12-14, 2012, Jilin, China

Mechatronics and Information Technology Qing Kai Han, Kazuhiko Takahashi, Chang Hyun Oh, Zhong Luo, 2011-12-22
Selected, peer reviewed papers from the 2011 International Conference on Mechatronics and Information Technology, (ICMIT 2011), August 16-19, 2011, Shenyang, China

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