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ha0c a1b1 and c 0 a0

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submission deadline is on 9 28 by 6  
00pm write simulation for a multiplier

the input output ports of the 8 bit  
multiplier is as follows module  
seqmultiplier input wire clk input wire

enable input wire 7 0 a input wire 7 0 b  
output wire 15 0 c