

Les Gens Qui Doutent Chords

Robert R. Redfield

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multiplier a0 a1 b0 b1 c 3 0 output 3 0

c input a0 a1 b0 b1 wire a0b1 a1b0

ha0c a1b1 and c 0 a0

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circuit that takes two numbers as input

and produces their product as an

output so a binary multiplier takes

binary numbers as inputs and produces

a result in binary before moving

forward lets quickly recap binary

multiplication first 0 x 0 0 0 x 1 0 1 x 0

0 1 x 1 1 so there s always a confusion

in students

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web the verilog arithmetic operators all

produce full precision results e g

adding two 8 bit numbers produces a 9

bit result in many designs one chooses

a word size many computers use 32 or

64 bits and all arithmetic results are

truncated to that number of bits i e

arithmetic is performed modulo 2word size

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web sequential multiplier assume the

multiplicand a has n bits and the

multiplier b has m bits if we only want

to invest in a single n bit adder we can

build a sequential circuit that processes

a single partial product at a time and

then cycle the circuit m times p b a s n

nc n xn n n 1 s n 1 s 0 init p 0 load a

and b repeat m times

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multiplier you will probably want to use

bit concatenation to form the input bits

wire 3 0 a wire 3 0 b case a b endcase

and your case statement will have 256

lines in it

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only the multiplicand changes not the

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bit signed sequential multiplier using

verilog the inputs are clk clock rst reset

a 8 bit multiplier b 8 bit multiplicand

and the outputs are p product and rdy

ready signal indicating multiplication is over

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web you cannot use the multiplication operator of verilog the lab file

submission deadline is on 9 28 by 6 00pm write simulation for a multiplier the input output ports of the 8 bit

multiplier is as follows module

seqmultiplier input wire clk input wire

enable input wire 7 0 a input wire 7 0 b

output wire 15 0 c